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(54) PLL circuit having a ring oscillator and method of controlling the same

(57) A PLL circuit detects oscillation halt of a voltage control oscillator, generates an oscillation control signal for automatically oscillating the voltage control oscillator based upon the detected signal, and automatically restores the voltage control oscillator to a normal oscillation state by the use of the generated signal. The voltage control oscillator is structured by a ring oscillator in which a plurality of differential amplifiers are connected in a ring form. A plurality of oscillation control means are arranged for the respective inputs of the differential amplifiers so as to set the ring oscillator into an oscillation state when the voltage control oscillator halts. The oscillation control means is controlled by the oscillating control signal.

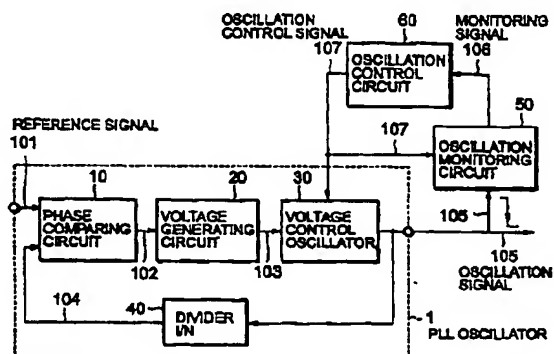


FIG.4

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EP 99 11 6166

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			H03L H03B
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 4 July 2000	Examiner Kahn, K-D
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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ANNEX TO THE EUROPEAN SEARCH REPORT
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